

User's Guide

NHD-240128BZ-NSW-BTW LCM

(Liquid Crystal Display Graphic Module)
RoHS Compliant

NHD- Newhaven Display
240128- 240 x 128 pixels
BZ- Version Line
N- Transmissive

SW- Side White LED B/L B- STN-(negative)Blue

T- 12:00 View

W- Wide Temperature $(-20 \sim +70c)$

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1.Features

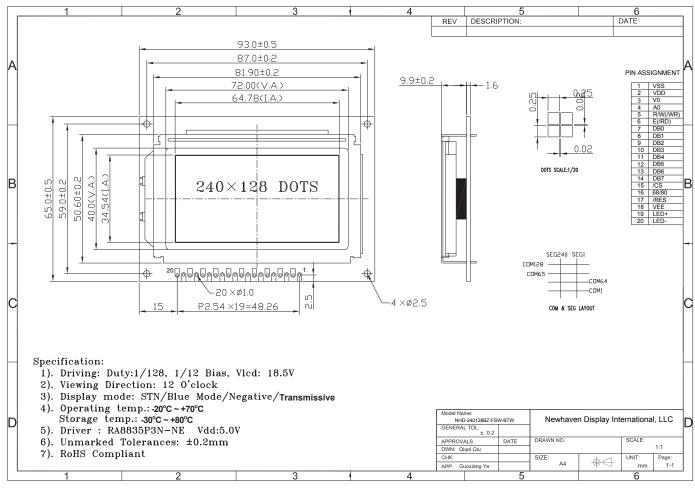
- 1. 240X128 dots
- 2. Built-in controller (RA8835P3N)
- 3. +5V power supply
- 4. 1/128 duty cycle;1/12bias5. BKL to be driven by pin9, pin20

LCD type	□FSTN positive			□FSTN Negative	
	□STN Yellow (Green	□STN	Gray	☑STN-Blue
View direction	□6 O'clock		☑12 C)'clock	
Rear Polarizer	□Reflective		☑Tran	smissive	□Transflective
Backlight Type	☑LED			□Internal Power	□4.2V input
		□CCFL	_	☑External Power	☑3.3 input
Backlight Color	☑White □Ambe		er	□Blue-Green	□Yellow-Green
Temperature Range	☑Wide		□Standard		☐Super Wide
DC to DC circuit	☑Build-in			□Not Build-in	
El Driver IC	□Build-in			☑Not Build-in	
Touch screen	□With			☑Without	
Font type	☑English-Jap □Englis		sh-Eur	□English-Russian	□other
	anese	open			

2. MECHANICAL SPECIFICATIONS

Module size	93.0mm(L)*65.0mm(W)* Max13.5(H)mm
Viewing area	72.0mm(L)*40.0mm(W)
Dots size	0.25mm(L)*0.25mm(W)
Dots pitch	0.27mm(L)*0.27mm(W)
Weight	Approx.

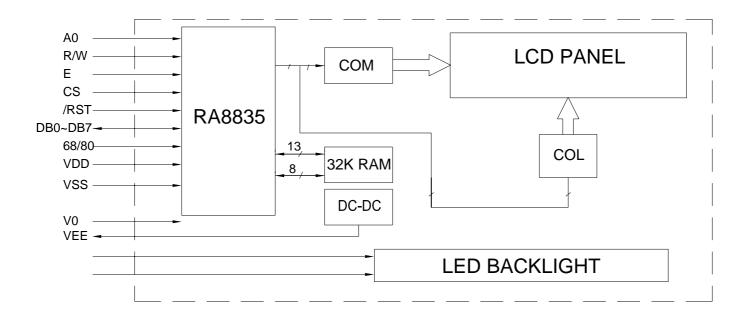
3. Outline dimension



4. Absolute maximum ratings

Item	Symbol		Standard		Unit
Power voltage	V _{DD} -V _{SS}	0	-	7.0	V
Input voltage	V_{IN}	VSS	-	VDD	V
Operating temperature range	V_{OP}	-20	-	+70	
Storage temperature range	V _{ST}	-30	-	+80	

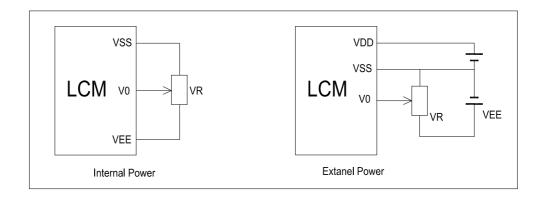
5.Block diagram



6.Interface pin description

Pin no.	Symbol	External connection	Function
1	Vss		Signal ground for LCM (GND)
2	V_{DD}	Power supply	Power supply for logic (+5V) for LCM
3	V_0		Contrast adjust
4	A0	MPU	Register select signal
5	R/W	MPU	Read/write select signal
6	E	MPU	Operation (data read/write) enable signal
7~10	DB0~DB3	MPU	Four low order bi-directional three-state data bus lines. Used for data transfer between the MPU and the LCM. These four are not used during 4-bit operation.
11~14	DB4~DB7	MPU	Four high order bi-directional three-state data bus lines. Used for data transfer between the MPU
15	/CS	MPU	Chip select
16	68/80	MPU	0: 8080 Family 1: 6800 Family
17	/RESET	MPU	Reset signal
18	VEE	OUT	DC-DC OUT
19	LED+	LED BKL power	Power supply for BKL
20	LED-	supply	Power supply for BKL

7.Contrast adjust



 $V_0 \sim V_{SS}$: LCD Driving voltage

VR: 10k~20k

8. Optical characteristics

STN type display module (Ta=25 , VDD=5.0V)

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Viewing angle		C 2	-60	-	35	dog
		C _r 2	-40	-	40	deg
Contrast ratio	Cr		-	6	-	-
Response time (rise)	Tr	-	-	150	250	mc
Response time (fall)	Tr	-	-	150	250	ms

9. Electrical characteristics

DC characteristics

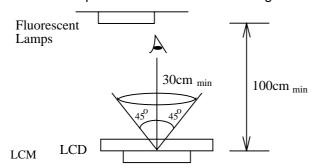
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Supply voltage for LCD	V ₀	Ta =25	-	12	-	V
Input voltage	$V_{ extsf{DD}}$		4.7	5.0	5.5	
Supply current	l _{DD}	Ta=25 , V _{DD} =5.0V	-	3.5	5.5	mA
Input leakage current	I _{LKG}		-	-	1.0	uA
"H" level input voltage	ViH		2.2	-	V _{DD}	
"L" level input voltage	VIL	Twice initial value or less	0	-	0.6	
"H" level output voltage	Vон	LOH=-0.25mA	2.4	-	-	V
"L" level output voltage	Vol	LOH=1.6mA	-	-	0.4	
Backlight supply voltage	VF		-	3.2	-	
Backlight supply current	I _{LED}	V _{F=} 3.2V	-	36	-	mA

10.QUALITY SPECIFICATIONS

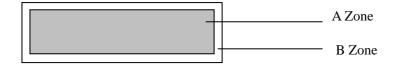
10.1 Standard of the product appearance test

Manner of appearance test: The inspection should be performed in using 20W x 2 fluorescent lamps. Distance between LCM and fluorescent lamps should be 100 cm or more. Distance between LCM and inspector eyes should be 30 cm or more.

Viewing direction for inspection is 45° from vertical against LCM.



Definition of zone:



A Zone: Active display area (minimum viewing area).

B Zone: Non-active display area (outside viewing area).

10.2 Specification of quality assurance AQL inspection standard

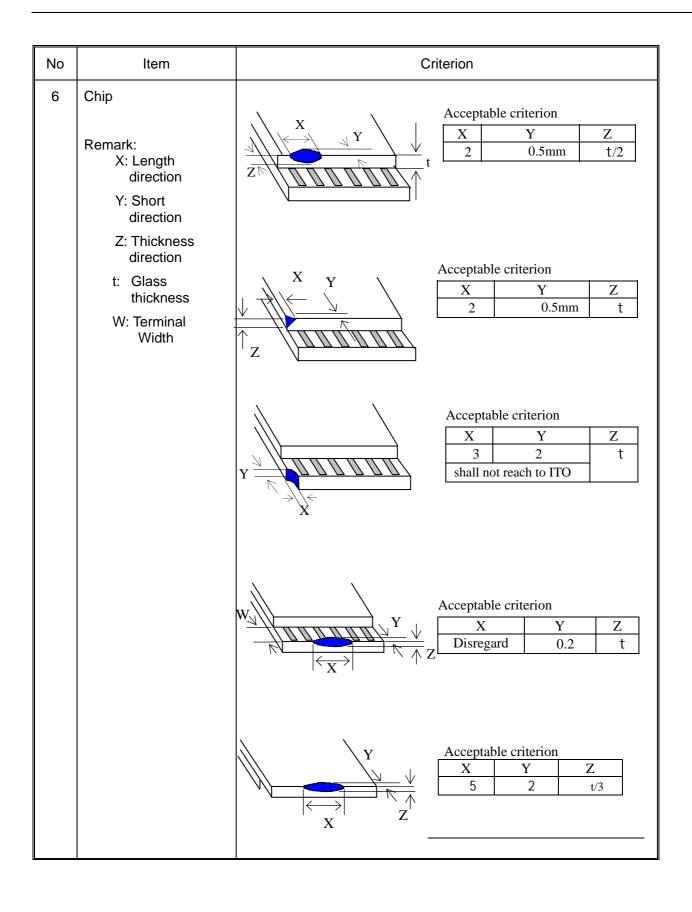
Sampling method: MIL-STD-105E, Level II, single sampling

Defect classification (Note: * is not including)

Classify		Item	Note	AQL
Major	Display state	Short or open circuit	1	0.65
		LC leakage		
		Flickering		
		No display		
		Wrong viewing direction		
		Contrast defect (dim, ghost)	2	
		Back-light	1,8	
	Non-display	Flat cable or pin reverse	10	
		Wrong or missing component	11	
Minor	Display	Background color deviation	2	1.0
	state	Black spot and dust	3	
		Line defect, Scratch	4	
		Rainbow	5	
		Chip	6	
		Pin hole	7	
		Protruded	12	
	Polarizer	Bubble and foreign material	3	
	Soldering	Poor connection	9	
	Wire	Poor connection	10	
	TAB	Position, Bonding strength	13	

Note on defect classification

No.	Item	Criterion
1	Short or open circuit	Not allow
	LC leakage	
	Flickering	
	No display	
	Wrong viewing direction	
	Wrong Back-light	
2	Contrast defect	Refer to approval sample
	Background color deviation	
3	Point defect, Black spot, dust (including Polarizer) $\phi = (X+Y)/2$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$
4	Line defect, Scratch	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$
5	Rainbow	Not more than two color changes across the viewing area.



No.	Item	Criterion
7	Segment pattern W = Segment width $\phi = (X+Y)/2$	(1) Pin hole $\phi < 0.10$ mm is acceptable. X
8	Back-light	(1) The color of backlight should correspond its specification.
9	Soldering	(2) Not allow flickering (1) Not allow heavy dirty and solder ball on PCB. (The size of dirty refer to point and dust defect) (2) Over 50% of lead should be soldered on Land. Lead 50% lead
10	Wire	 (1) Copper wire should not be rusted (2) Not allow crack on copper wire connection. (3) Not allow reversing the position of the flat cable. (4) Not allow exposed copper wire inside the flat cable.
11*	PCB	(1) Not allow screw rust or damage.(2) Not allow missing or wrong putting of component.

No	Item	Criterion
12	Protruded W: Terminal Width	Acceptable criteria: $Y \le 0.4$
13	TAB	1. Position W W H H 1/3W H H 1/3H TAB 2 TAB bonding strength test F TAB P (=F/TAB bonding width) 5pcs per SOA (shipment) 650gf/cm ,(speed rate: 1mm/min)
14	Total no. of acceptable Defect	A. Zone Maximum 2 minor non-conformities per one unit. Defect distance: each point to be separated over 10mm B. Zone It is acceptable when it is no trouble for quality and assembly in customer's end product.

10.3 Reliability of LCM

Reliability test condition:

Item	Condition	Time (hrs)	Assessment
High temp. Storage	80°C	48	
High temp. Operating	70°C	48	No abnormalities
Low temp. Storage	-30°C	48	in functions
Low temp. Operating	-20°C	48	and appearance
Humidity	40°C/ 90%RH	48	
Temp. Cycle	0° C ← 25° C → 50° C (30 min ← 5 min → 30min)	10cycles	

Recovery time should be 24 hours minimum. Moreover, functions, performance and appearance shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature (20±8°C), normal humidity (below 65% RH), and in the area not exposed to direct sun light.

14.4 Precaution for using LCD/LCM

LCD/LCM is assembled and adjusted with a high degree of precision. Do not attempt to make any alteration or modification. The followings should be noted.

General Precautions:

- 1. LCD panel is made of glass. Avoid excessive mechanical shock or applying strong pressure onto the surface of display area.
- 2. The polarizer used on the display surface is easily scratched and damaged. Extreme care should be taken when handling. To clean dust or dirt off the display surface, wipe gently with cotton, or other soft material soaked with isoproply alcohol, ethyl alcohol or trichlorotriflorothane, do not use water, ketone or aromatics and never scrub hard.
- 3. Do not tamper in any way with the tabs on the metal frame.
- 4. Do not make any modification on the PCB without consulting Newhaven
- 5. When mounting a LCM, make sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.
- 6. Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels and also cause rainbow on the display.
- 7. Be careful not to touch or swallow liquid crystal that might leak from a damaged cell. Any liquid crystal adheres to skin or clothes, wash it off immediately with soap and water.

Static Electricity Precautions:

1. CMOS-LSI is used for the module circuit; therefore operators should be grounded whenever he/she comes into contact with the module.

- 2. Do not touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.
- 3. Do not touch the connection terminals of the display with bare hand; it will cause disconnection or defective insulation of terminals.
- 4. The modules should be kept in anti-static bags or other containers resistant to static for storage.
- 5. Only properly grounded soldering irons should be used.
- 6. If an electric screwdriver is used, it should be grounded and shielded to prevent sparks.
- 7. The normal static prevention measures should be observed for work clothes and working benches.
- 8. Since dry air is inductive to static, a relative humidity of 50-60% is recommended.

Soldering Precautions:

- 1. Soldering should be performed only on the I/O terminals.
- 2. Use soldering irons with proper grounding and no leakage.
- 3. Soldering temperature: 280°C±10°C
- 4. Soldering time: 3 to 4 second.
- 5. Use eutectic solder with resin flux filling.
- 6. If flux is used, the LCD surface should be protected to avoid spattering flux.
- 7. Flux residue should be removed.

Operation Precautions:

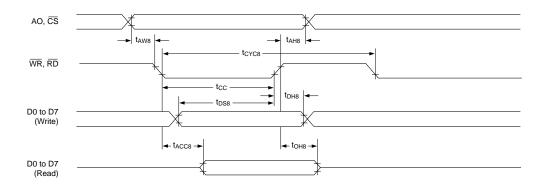
- 1. The viewing angle can be adjusted by varying the LCD driving voltage Vo.
- 2. Since applied DC voltage causes electro-chemical reactions, which deteriorate the display, the applied pulse waveform should be a symmetric waveform such that no DC component remains. Be sure to use the specified operating voltage.
- 3. Driving voltage should be kept within specified range; excess voltage will shorten display life.
- 4. Response time increases with decrease in temperature.
- 5. Display color may be affected at temperatures above its operational range.
- 6. Keep the temperature within the specified range usage and storage. Excessive temperature and humidity could cause polarization degradation, polarizer peel-off or generate bubbles.
- 7. For long-term storage over 40°C is required, the relative humidity should be kept below 60%, and avoid direct sunlight.

Limited Warranty

Newhaven's LCDs and modules are not consumer products, but may be incorporated by Newhaven's customers into consumer products or components thereof, Newhaven does not warrant that its LCDs and components are fit for any such particular purpose.

- 1. The liability of Newhaven is limited to repair or replacement on the terms set forth below. Newhaven will not be responsible for any subsequent or consequential events or injury or damage to any personnel or user including third party personnel and/or user. Unless otherwise agreed in writing between Newhaven and the customer, Newhaven will only replace or repair any of its LCD which is found defective electrically or visually when inspected in accordance with Newhaven general LCD inspection standard. (Copies available on request)
- 2. No warranty can be granted if any of the precautions state in handling liquid crystal display above has been disregarded. Broken glass, scratches on polarizer mechanical damages as well as defects that are caused accelerated environment tests are excluded from warranty.
- 3. In returning the LCD/LCM, they must be properly packaged; there should be detailed description of the failures or defect.

Timing Diagrams 8080 family interface timing



Ta = -20 to 75° C

Cianal	Symbol	Parameter	VDD = 4.5	5 to 5.5V	VDD = 2.7	7 to 4.5V	Unit	Condition
Signal	Symbol	Parameter	min max		min	max	Onit	Condition
A0, CS	tAH8	Address hold time	10	_	10		ns	
AU, CS	tAW8	Address setup time	0	_	0		ns	
WR, RD	tCYC8	System cycle time	See note.	_	See note.		ns	
WK, KD	tcc	Strobe pulsewidth	120	_	150	_	ns	CL = 100pF
	tDS8	Data setup time	120	_	120	_	ns	CL = 100pi
D0 to D7	tDH8	Data hold time	5	_	5	_	ns	
וטטוטטן	tACC8	RD access time	_	50	_	80	ns	
	tOH8	Output disable time	10	50	10	55	ns	

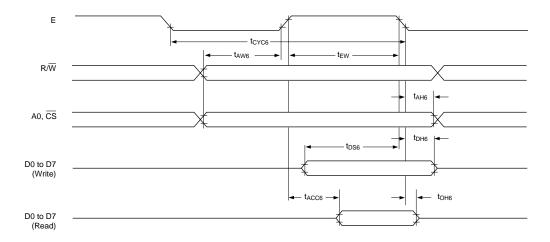
Note: For memory control and system control commands:

 $t_{CYC8} = 2t_{C} + t_{CC} + t_{CEA} + 75 > t_{ACV} + 245$

For all other commands:

 $t_{\rm CYC8} = 4t_{\rm C} + t_{\rm CC} + 30$

6800 family interface timing



Note: tcyc6 indicates the interval during which CS is LOW and E is HIGH.

 $Ta = -20 \text{ to } 75^{\circ}\text{C}$

Cianal	Symbol	Parameter	VDD = 4.5	5 to 5.5V	VDD = 2.7	7 to 4.5V	Unit	Condition
Signal	Syllibol	Parameter	min	max	min	max	Onit	Condition
A0,	tCYC6	System cycle time	See note.	_	See note.		ns	
A0, CS,	tAW6	Address setup time	0	_	10	_	ns	
R/W tah	tAH6	Address hold time	0	_	0	_	ns	
	tDS6	Data setup time	100	_	120	_	ns	CL =
D0 to D7	tDH6	Data hold time	0	_	0	_	ns	100 pF
D0 to D7	tOH6	Output disable time	10	50	10	75	ns	
	tACC6	Access time	_	85	_	130	ns	
Е	tEW	Enable pulsewidth	120	_	150	_	ns]

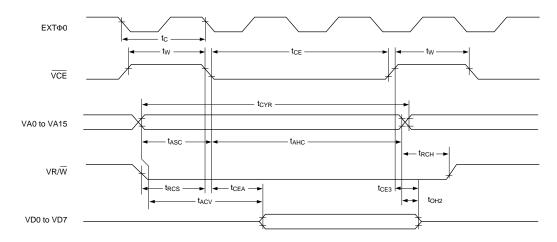
Note: For memory control and system control commands:

 $t_{CYC6} = 2t_{C} + t_{EW} + t_{CEA} + 75 > t_{ACV} + 245$

For all other commands:

 $t_{\rm CYC6} = 4t_{\rm C} + t_{\rm EW} + 30$

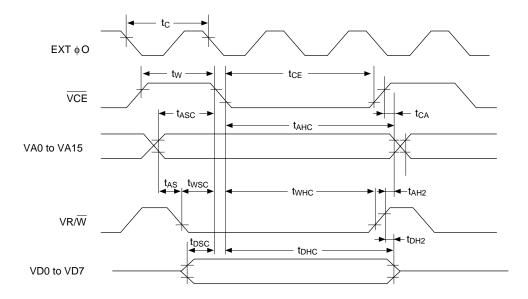
Display memory read timing



 $Ta = -20 \text{ to } 75^{\circ}C$

Cianal	Symbol	Parameter	VDD = 4.	5 to 5.5V	VDD = 2.	7 to 4.5V	Unit	Condition
Signal S	Symbol	Parameter	min	max	min	max	Onit	
ΕΧΤ φ0	tC	Clock period	100	_	125	_	ns	
VCE	tw	VCE HIGH-level pulsewidth	tc - 50	_	tc - 50	_	ns	
VCE	tCE	VCE LOW-level pulsewidth	2tc - 30	_	2tc - 30	_	ns	
	tCYR	Read cycle time	3tC	_	3tC	_	ns	
VA0 to VA15	tASC	Address setup time to falling edge of VCE	tc - 70	_	tc - 100	_	ns	
	tAHC	Address hold time from falling edge of VCE	2tc - 30	_	2tc - 40	_	ns	CL = 100 pF
VRD	tRCS	Read cycle setup time to falling edge of VCE	tc - 45	_	tc - 60	_	ns	
	tRCH	Read cycle hold time from rising edge of VCE	0.5tC	_	0.5tC	_	ns	
	tACV	Address access time	_	3tc - 100	_	3tC - 115	ns	
VD0 to	tCEA	VCE access time	_	2tc - 80	_	2tc - 90	ns	
VD7	tOH2	Output data hold time	0	_	0	_	ns]
	tCE3	VCE to data off time	0	_	0	_	ns	

Display memory write timing



INSTRUCTION SET The Command Set

Table 1. Command set

Class Command			Code									Hex	Command Description	Command Read Parameters		
		RD	WR	A0	D7	D6	D5	D4	D3	D2	D1	D0			No. of Bytes	Sec- tion
System	SYSTEM SET	1	0	1	0	1	0	0	0	0	0	0	40	Initialize device and display	8	8.2.1
Control	SLEEP IN	1	0	1	0	1	0	1	0	0	1	1	53	Enter standby mode	0	8.2.2
	DISP ON/OFF	1	0	1	0	1	0	1	1	0	0	D	58, 59	Enable and disable display and display flashing	1	8.3.1
	SCROLL	1	0	1	0	1	0	0	0	1	0	0	44	Set display start address and display regions	10	8.3.2
	CSRFORM	1	0	1	0	1	0	1	1	1	0	1	5D	Set cursor type	2	8.3.3
Display	CGRAM ADR	1	0	1	0	1	0	1	1	1	0	0	5C	Set start address of char acter generator RAM	2	8.3.6
control	CSRDIR	1	0	1	0	1	0	0	1	1	CD 1	CD 0	4C to 4F	Set direction of cursor movement	0	8.3.4
	HDOT SCR	1	0	1	0	1	0	1	1	0	1	0	5A	Set horizontal scroll position	1	8.3.7
	OVLAY	1	0	1	0	1	0	1	1	0	1	1	5B	Set display overlay format	1	8.3.5
Drawing	CSRW	1	0	1	0	1	0	0	0	1	1	0	46	Set cursor address	2	8.4.1
control	CSRR	1	0	1	0	1	0	0	0	1	1	1	47	Read cursor address	2	8.4.2
Memory	MWRITE	1	0	1	0	1	0	0	0	0	1	0	42	Write to display memory	_	8.5.1
control	MREAD	1	0	1	0	1	0	0	0	0	1	1	43	Read from display memory	_	8.5.2

Notes:

- In general, the internal registers of the SED1335 series are modified as each command parameter is input. However,
 the microprocessor does not have to set all the parameters of a command and may send a new command before all parameters
 have been input. The internal registers for the parameters that have been input will have been changed but the remaining
 parameter registers are unchanged.
 - 2-byte parameters (where two bytes are treated as 1 data item) are handled as follows:
 - a. CSRW, CSRR: Each byte is processed individually. The microprocessor may read or write just the low byte of the cursor address.
 - b. SYSTEM SET, SCROLL, CGRAM ADR: Both parameter bytes are processed together. If the command is changed after half of the parameter has been input, the single byte is ignored.
- 2. APL and APH are 2-byte parameters, but are treated as two 1-byte parameters.

Initialization procedure

No.	Command	Operation
1	Power-up	
2	Supply	
3	SYSTEM SET	
	C = 40H	
	P1 = 38H	M0: Internal CG ROM M1: CG RAM is 32 characters maximum M2: 8 lines per character W/S: Two-panel drive IV: No top-line compensation
	P2 = 87H	FX: Horizontal character size = 8 pixels WF: Two-frame AC drive
	P3 = 07H	FY: Vertical character size = 8 pixels
	P4 = 3FH	C/R: 64 display addresses per line
	P5 = 49H	TC/R: Total address range per line = 90 fosc = 6.0 MHz, fFR = 70 Hz
	P6 = 7FH	L/F: 128 display lines
4	P7 = 80H P8 = 00H SCROLL	AP: Virtual screen horizontal size is 128 addresses
	C = 44H	
	P1 = 00H P2 = 00H	First screen block start address Set to 0000H
	P3 = 40H	Display lines in first screen block = 64
	P4 = 00H P5 = 10H	Second screen block start address Set to 1000H
	P6 = 40H	Display lines in second screen block = 64
	P7 = 00H P8 = 04H	Third screen block start address Set to 0400H

No.	Command	Operation				
	P9 = 00H	Fourth screen block start address				
	P10 = 30H	Set to 3000H				
		Display memory				
		(SAD1) 0000H 1st display memory page				
		(SAD3) 0400H 2nd display memory page				
		0800H				
		(SAD2) 1000H				
		3rd display memory page				
		(SAD4) 3000H				
		4th display memory page				
		5000H				
5	HDOT SCR					
	C = 5AH					
	P1 = 00H	Set horizontal pixel shift to zero				
6	OVLAY					
	C = 5BH					
	P1 = 01H	MX 1, MX 0: Inverse video superposition				
	-	DM 1: First screen block is text mode				
		DM 2: Third screen block is text mode				
7	DISP ON/OFF					
	C = 58H	D: Display OFF				
	P1 = 56H	FC1, FC0: Flash cursor at 2 Hz				
		FP1, FP0: First screen block ON FP3, FP2: Second and fourth screen blocks ON				
		FP5, FP4: Third screen block ON				
8	Clear data in first layer	Fill first screen layer memory with 20H (space character)				

No.	Command	Operation
9	Clear data in second layer	Fill second screen layer memory with 00H (blank data)
		Display Character code in every position 1st layer Blank code in every position
10	CSRW C = 46H P1 = 00H	Set cursor to start of first screen block
	P2 = 00H	Set cursor to start or mist screen block
11	CSR FORM	
	C = 5DH	
	P1 = 04H P2 = 86H	CRX: Horizontal cursor size = 5 pixels CRY: Vertical cursor size = 7 pixels
	1 2 - 0011	CM: Block cursor
12	DISP ON/OFF	
	C = 59H	Display ON
		Display
4.0	005 515	
13	CSR DIR C = 4CH	Set cursor shift direction to right
	0 = 4011	Oct oursor striit uneotion to right

No.	Command	Operation
14	MWRITE	
	C = 42H	
	P1 = 20H	()
	P2 = 45H	(E)
	P3 = 50H P4 = 53H	'P'
	P5 = 4FH	'O',
	P6 = 4EH	'N'
		EPSON■
		LF30N=
15	CSRW	
	C = 46H	
	P1 = 00H	Set cursor to start of second screen block
	P2 = 10H	
16	CSR DIR	
	C = 4FH	Set cursor shift direction to down
17	MWRITE	
	C = 42H	
	P1 = FFH	Fill in a square to the left of the 'E'
	↓ 50 ==!	
	P9 = FFH	■EPSON
18	CSRW	
	C = 46H	
	P1 = 01H	Set cursor address to 1001H
	P2 = 10H	
19	MWRITE	
	C = 42H	

No.	Command	Operation
	P1 = FFH	Fill in the second screen block in the second column of line 1
	\	
	P9 = FFH	
20	CSRW	Repeat operations 18 and 19 to fill in the background under 'EPSON'
↓		
		Inverse display
29	MWRITE	
		EPSON
30	CSRW	
	C = 46H	
	P1 = 00H	Set cursor to line three of the first screen block
	P2 = 01H	
31	CSR DIR	
	C = 4CH	Set cursor shift direction to right
32	MWRITE	
	C = 42H	
	P1 = 44H	'D'
	P2 = 6FH	΄ο΄
	P3 = 74H P4 = 20H	't', Inverse display
	P5 = 4DH	'M'
	P6 = 61H	'a' EPSON
	P7 = 74H P8 = 72H	'Ψ'
	P9 = 69H	gr
	P10 = 78H	'x', Dot matrix LCD
	P11 = 20H	(1)
	P12 = 4CH P13 = 43H	,C,
	P14 = 44H	'D'